

REMARKS

A. Pending Claims

Claims 1-3 and 5 are pending in this application for reconsideration. The Examiner has rejected all pending claims in an Office Action mailed July 6, 2006. Favorable reconsideration of this application as presented herein is requested in light of Applicants' remarks below. Applicants also file herewith a Terminal Disclaimer.

B. Office Action Mailed July 6, 2006

In the Office Action mailed July 6, 2006, the Examiner has maintained many of the rejections articulated in the Examiner's January 12, 2006 Office Action. The Examiner has again provisionally rejected claims 1-4 on nonstatutory-obvious-type double patenting over claims 1-10 of copending U.S. Application No. 11/249,976. In response, Applicants submit herewith a terminal disclaimer signed by Applicants' attorney of record. The Examiner has rejected claims 1 and 5 under 35 U.S.C. § 103 over U.S. Patent No. 6,125,286 to Jahagirdar et al. ("Jahagirdar") in view of Japanese Patent No. 405014540A to Yoshida et al. ("Yoshida"). Claim 2 is rejected over Jahagirdar in view of Yoshida and further in view of U.S. Patent No. 5,077,832 to Szczutkowski et al ("Szczutkowski"). Claim 3 is rejected over Jahagirdar in view of Yoshida and further in view of U.S. Patent No. 6,035,180 to Kubes et al. ("Kubes").

In response, Applicants have discussed and distinguished, below, Jahagirdar and Yoshida as not disclosing alone or in combination the independently controlled first address data bus and second address data bus of the claims. Furthermore, Applicants' distinguish Szczutkowski and Kubes as failing to disclose Applicants' claimed invention. Thus, Applicants respectfully submit that the claims are patentably distinct and allowable over the cited art.

In again rejecting the claims as obvious, the Examiner alleges that Yoshida teaches, in an analogous art, that a second address data bus (44; Fig. 4) for connecting said control means and said display (36; Fig. 4) means independently of said first address data; (42; Fig. 4) wherein said control means controls said first address data bus and said second address data bus independently (see July 6, 2006, Office Action at p. 4). Further, in responding to Applicants' arguments, the Examiner states that "Yoshida asserts so many lines or buses, one of them is a data bus for example buses 39-41, 36, 42, 44, consequently, Yoshida emphasizes an address data bus. (Please perceive busses 39-41, 36, 42, 44; Abstract). Hence, it is believed that Yoshida **still teaches the claimed limitations**" (see Office Action at p. 9 (emphasis in original)).

As stated above, the Examiner points that one of, for example, buses 39-41, 36, 42, 44 is a data bus. However, from this list, only reference numeral 42 is arguably a data bus. Reference numeral 36 is a display and reference numeral 44 is a signal line. Therefore, contrary to the Examiner's assertion, reference numeral 44 (Fig. 4) is not a second address data bus.

Thus, Yoshida fails to disclose "a second address data bus for connecting said control means and said display means independently of said first address data bus" as recited in claim 1.

Although Szczutkowski and Kubes are in an analogous art with Jahagirdar and Yoshida, Szczutkowski fails to disclose the mobile telephone of claim 1, further comprising "communication means for transmitting and receiving signals via a radio line wherein said control means is controlled to prohibit access to said second address data bus while said communication means is receiving signals", as recited in dependent claim 2, for example, and, and Kubes fails to disclose the mobile telephone as claimed in claim 1, wherein said second

address data bus is driven by lower voltage than said first address data bus, as recited in, for example dependent claim 3.

Neither Szczutkowski nor Kubes teaches or suggests the above limitation in the present claimed invention. Furthermore, there is no motivation for combining Szczutkowski or Kubes with Jahagirdar and Yoshida. The inventions, as claimed, are not obvious over Szczutkowski nor Kubes.

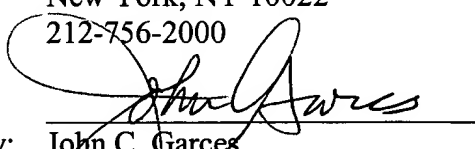
Conclusion

For at least the reasons set forth above, Applicants respectfully submit that this patent application, as amended, is in condition for allowance. The Examiner is urged to telephone Applicants' undersigned counsel at the number provided below if it will advance the prosecution of this application. Reconsideration and prompt allowance of this patent application are respectfully requested.

Respectfully submitted,

Schulte Roth & Zabel LLP
919 Third Avenue
New York, NY 10022
212-756-2000

Dated: October 6, 2006
New York, New York

By: 
John C. Garces
Attorney for Applicants
Reg. No. 40,616

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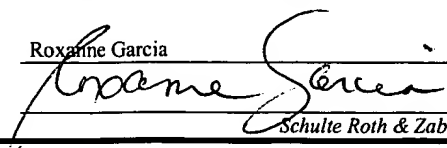
Date of Deposit: Oct 6, 2006

I hereby certify under 37 C.F.R. 1.8 that this correspondence and enumerated documents are being deposited with the United States Postal Service as First Class Mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Name:

Roxanne Garcia

Signature:


Schulte Roth & Zabel, LLP